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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10062999	01/30/2002	324		2858	
**APPLICANTS: Eldridge Benjamin; Miller Charles;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 10/003,596 10/30/2001 WHICH IS A DIV OF 09/484,600 01/18/2000 PAT 6,339,338					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>V. Nguyen</i>				ATTORNEY DOCKET NO P102C1-US	
TITLE : Apparatus for reducing power supply noise in an integrated circuit					
U.S. DEPT. OF COMMERCE / PAT. & TRADEMARK OFFICE (Rev. 12-94)					

7/10/03 Formal Drawings (11 shs) set 1 1/30/02

NOTICE OF ALLOWANCE MAILED 2/24/03		VINCENT G. NGUYEN Assistant Examiner		CLAIMS ALLOWED Total Claims 31 Print Claim for O.G. 1	
ISSUE FEE Amount Due 1600.00 Date Paid		N. Le Supervisory Patent Examiner Technology Center Examiner		DRAWING Sheets Drwg. 11 Figs. Drwg. 21 Print Fig. 18	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner 2/26/03	
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